**KEY FEATURES:**

- Eight single-ended or true differential inputs
- Programmable input ranges of: 0-5V, 0-10V, $\pm 5V$, $\pm 10V$
- 100KHz sampling rate
- On-board pacer clock and counter timers
- Four double-buffered analog outputs
- Jumper selectable output ranges of: 0-5V, 0-10V, $\pm 5V$, $\pm 10V$
- 24 digital I/O lines, type 82C55 with change of state detect on port C, buffers on ports A & B
- Flexible configurations to suit your needs

FACTORY OPTIONS:

- 4-20mA inputs with offset
- Channel by channel pre-amplifier gains of 1-100
- +5VDC only operation
- 0 to +70°C and -40 to +85°C versions available

The 104-AIO12-8 is a low-cost 12-channel analog multifunction I/O board which features an excellent price/performance value for PC/104-based data acquisition. The inherent "selectability" of the card's onboard features allows the depopulation of unnecessary functionality. This keeps costs down by allowing the user to more precisely specify the board to the application's unique requirements.

The 104-AIO12-8 provides eight single-ended or eight true differential analog input channels with 12-bit resolution. 200V common-mode rejection, high input impedance (2MegOhms, typical) and factory pre-settable gain to accommodate low-level sensor inputs are also included. Analog inputs are software programmable for 0-5V, 0-10V, $\pm 5V$ and $\pm 10V$, and optionally factory configurable for 4-20mA. The same ranges are jumper-selected for the four channels of 12-bit analog output. 24 parallel lines of digital I/O, eight of which also provide change-of-state detection, are also provided for a complete, low cost, multifunction data acquisition solution.

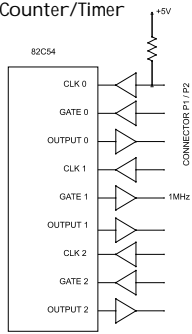
SOFTWARE

The 104-AIO12-8 is supported for use in all operating systems and include a free DOS, Linux and Windows 95/98/Me/NT/2000/XP compatible software package. This includes sample programs and source code in "C" and Pascal for DOS, and Visual Basic, Delphi, C++ Builder, and Visual C++ for Windows. Also included is a graphical setup program in Windows. Linux support includes installation files and basic samples for programming from any user level via an open source kernel driver.

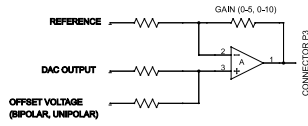


Block Diagram & Pin Configuration

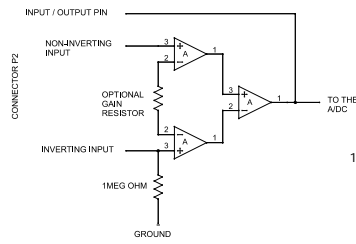
Counter/Timer



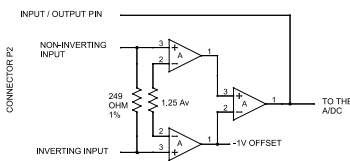
D/A Output Stage



Standard Input Stage



4 to 20mA Input Stage



Analog Input

VRef	01	02	VRef Adj.
AGND	03	04	AIN0+
AIN0-	05	06	AIN0 S.E.
AGND	07	08	AIN1 S.E.
AIN1-	09	10	AIN1+
AGND	11	12	AIN2+
AIN2-	13	14	AIN2 S.E.
AGND	15	16	AIN3 S.E.
AIN3-	17	18	AIN3+
AGND	19	20	AIN4+
AIN4-	21	22	AIN4 S.E.
AGND	23	24	AIN5 S.E.
AIN5-	25	26	AIN5+
AGND	27	28	AIN6+
AIN6-	29	30	AIN6 S.E.
AGND	31	32	AIN7 S.E.
AIN7-	33	34	AIN7+
NC	35	36	NC
NC	37	38	NC
NC	39	40	NC
GND	41	42	CLKO IN
GATE0 IN	43	44	OUT0
1MHz OUT	45	46	GATE1 IN
OUT1	47	48	CLK2 IN
GATE2 IN	49	50	OUT2

Analog Output

GND	01	02	AOUT0
GND	03	04	AOUT1
GND	05	06	VRef
GND	07	08	AOUT2
GND	09	10	AOUT3

Digital I/O

PC7	01	02	GND
PC6	03	04	GND
PC5	05	06	GND
PC4	07	08	GND
PC3	09	10	GND
PC2	11	12	GND
PC1	13	14	GND
PC0	15	16	GND
PB7	17	18	GND
PB6	19	20	GND
PB5	21	22	GND
PB4	23	24	GND
PB3	25	26	GND
PB2	27	28	GND
PB1	29	30	GND
PB0	31	32	GND
PA7	33	34	GND
PA6	35	36	GND
PA5	37	38	GND
PA4	39	40	GND
PA3	41	42	GND
PA2	43	44	GND
PA1	45	46	GND
PA0	47	48	GND
	49	50	GND

Specifications

A/D

Number of inputs	8 single-ended or 8 true differential
Resolution	12-bit
Bipolar ranges	±5V, ±10V (4-20mA factory option)
Unipolar ranges	0-5V, 0-10V
Sampling rate	100 KHz
Type	Successive Approximation
Nonlinearity	±1 LSB max, monotonic
Common mode voltage	±200V
Trigger source	Software selectable: programmable timer, program command

Digital I/O

Number of I/O	24, pulled up to +5V
Type	82C55A
Input voltage	Logic low: -0.3V min, 0.8V max; Logic high: 2.2V min, 5.8V max
Input current	±1µA max
Outputs	Logic low: 0.0V min, 0.4V max; Logic high: 3.7V min, 5.0V max
Output current (Ports A & B)	Logic low: 64mA max sink; Logic high: 32mA max source
Output current (Ports C)	Logic low: 2.5mA max sink; Logic high: 2.5mA max source
Change of state	Port C enabled with change of state detection

D/A

Number of outputs	4
Resolution	12-bit resolution
Bipolar ranges	±5V, ±10V
Unipolar ranges	0-5V, 0-10V
Conversion rate	100 KHz
Relative accuracy	±2 LSB
Output current	3mA per channel

Counter/Timer

Type	82C54
Counters/timers	3 x 16-bit
Clock Frequency	1MHz
Software support	Event counter, frequency output, frequency pulse and measurement

General

Power required (Using optional DC/DC converter)	+5V @ 240mA typ
Power required (Using ±12V and +5V)	+12VDC: 30mA typical; -12VDC: 30mA typical; +5VDC: 40mA typical
Interrupt requests	IRQs 3-7, 9-12, 14, 15
Operating Temperature	0 to +70°C, optional -40 to +85°C
Storage Temperature	-50 to +120°C
Humidity	5% to 95% RH, non-condensing

104-AI012-8	12-bit, 8-channel A/D, 4 analog outputs and 24 digital I/O
104-AO12-4	12-bit, 4 analog outputs and 24 digital I/O (no inputs)
104-AI12-8	12-bit, 8-channel A/D and 24 digital I/O (no outputs)



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