

PCI-COM422/4 AND PCI-COM422/4S1

Serial Interface Cards

FEATURES

- Universal PCI, PCI-X, 3.3V and 5V compatible
- · Four-port serial communications card for PCI bus
- Supports RS-422 protocol
- Includes type 16550 UART with 16-byte FIFO buffers
- Baud rates up to 460,800 baud
- · Choice of I/O connection methods
- · Detected as standard COM ports by Windows
- No base address or IRQ switches to set



MANUALS PCI-COM-422-4.PDF

ACCESSORIES PCI-COM422/4

ACCESSORIES PCI-COM422/4S1

FUNCTIONAL DESCRIPTION

The PCI-COM422/4 is a four-port, asynchronous serial communications card designed for use in PCI-Bus computers. It supports RS-422 communications. The card is 6.1 inches (155 mm) long and may be installed in any 5-volt PCI slot in IBM and compatible computers.

Type 16550 UARTs are used as the asynchronous communication elements. These include a 16-byte transmit/receive FIFO buffer to protect against lost data in multitasking systems while maintaining 100 percent compatibility with the original IBM serial port.

A crystal oscillator is located on the card and permits precise baud rate capability up to 115,200. Higher baud rates, up to 460,800 baud, are achieved by changing a jumper on the card. The driver/receivers used, type 75176, are capable of driving extremely long communication lines at high baud rates. They can drive up to ±60 mA on balanced lines and receive inputs as low as 200 mV differential signal superimposed on common mode noise of +12V to -7V. In case of communication conflict, the driver/receivers feature thermal shutdown.

When the card is first installed, Windows will detect it as new hardware and assign it an IRQ number and base address. There are no switches to set or base addresses to assign, making it easy to use. From this point on, the card behaves as standard COM ports at COM 5,6,7, and 8. You cannot set or change the card's base address, you can only determine what the system has assigned. The PCI Bus supports 64K of I/O address space so your card's address may be located anywhere in the 0000 to FFFF range.

INPUT/OUTPUT CONNECTIONS

There are two I/O connector configurations. Model PCI-COM422/4 is the standard version and has a male DB25 connector on the mounting bracket and a breakout cable provides four DB9 male connectors. This version only requires one I/O slot. The second configuration, Model PCI-COM422/4 S1, has two DB9 connectors on the card mounting bracket (Ports A and B) and two more DB9 connectors on a separate mounting bracket (for Ports C and D) plus two ribbon cables that will mate with headers on the card.

Return to top of page

Specifications

Return to Product Description

Communications Interface

- I/O Connection: 9-pin D-sub connectors
- Serial Ports: Four shielded male D-sub 9-pin IBM AT style connectors compatible with RS-

422 specifications. (Note: On Model PCI-COM422/4 the breakout cable terminates with four female D-sub 9-pin connectors.)

- Character length: 5, 6, 7, or 8 bits.
- Parity: Even, odd or none.
- Stop Interval: 1, 1.5, or 2 bits.
- Serial Data Rates: Up to 115,200 baud, Asynchronous, A faster range of rates, up to 460,800, is achieved by jumper selection on the card. Type 16550 buffered UART.
- Address: Continuously mappable within 000 to FFFF (hex) range of PCI bus addresses.
- Receiver Input Sensitivity: ±200 mV, differential input.
- Common Mode Rejection: +12V to -7V
- Transmitter Output Drive Capability: 60 mA, with thermal shutdown.

Environmental

- Operating Temperature Range: 0° to +60° C
- Storage temperature Range: -50° to +120° C
- Humidity: 5% to 90%, non-condensing.
- Power Required: +5VDC at 125 mA typical, -12VDC at 5 mA typical, +12VDC at 5 mA typical, 750 mW total power consumption.
- Size: 6.1" (155 mm) long by 3.9" (99 mm).

Regulatory Compliance C €

Declaration of Conformity, and Test Reports are on file. Users must use appropriate shielded cables.

Part Number	Price(USD)
PCI-COM422/4	294.00
PCI-COM422/4S1	259.00

View / Download Manual (in .PDF format)

Return to top

Acquisition Control Communications Engineering / Systems